GET
Technical workshop - Bordeaux

Nov. 18-19, 2009

Nov. 18 08:45-17:30

- 08:45-09:00 **Introduction** (E. Pollacco, JL Pedroza)
- 09:00-09:45 **AGET** (P. Baron)
  - ✓ Architecture
  - ✓ Fast control /Hits details
  - ✓ Status
- 09:45-10:30 **AsAd** (J. Pibernat)
  - ✓ Architecture
  - ✓ Tasks and organisation
  - ✓ Status
- 10:30 -10:45 Break
- 10:45-11:15 **ZAP** (Animator JL Pedroza)
  - ✓ Status (E. Delagnes)
  - ✓ ZAP-AsAd coupling (JL Pedroza)
- 11:15-12:00 **CoBo** (N. Usher)
  - ✓ Reduced CoBo architecture
  - ✓ Firmware
  - ✓ Embedded software
  - ✓ DDR2 SDRAM access details
  - ✓ status
- 12:00-12:45 **MuTanT** (G. Wittwer)
  - ✓ MTCA architecture
  - ✓ CoBo-MuTanT coupling
  - ✓ MuTanT-MuTanT coupling
  - ✓ Clock distribution and inspection lines details
  - ✓ Status
- 12:45-14:00 Lunch
- 14:00-14:45 **Software Slow control** (S. Anvar)
  - ✓ Slow control distributed software architecture (over both embedded and workstation platforms)
  - ✓ Configuration management - interfacing run control, slow control and acquisition
  - ✓ Status of slow control and configuration frameworks
- 14:45-15:30 **Software Run control** (F. Saillant)
  - ✓ Run control architecture
  - ✓ Data flow and embedded readout
  - ✓ Status
- 15:30-15:45 Break
- 15:45-17:00 **Parallel sessions**
  - ✓ Hardware sub session discussion
    - ✓ Particulars points
    - ✓ Interfaces
  - ✓ Software sub session discussion
    - ✓ Particulars points
    - ✓ Interfaces
- 17:00-17:30 **Summary and conclusions**

Nov. 19 09:00-16:30

- 09:00- 09:30 2010 **Tasks schedule** (Animator JLPedroza)
  - ✓ Goal: Dec. 2010 GET prototype 1 Ready
  - ✓ Milestone Apr. 2010
- 09:30-10:30 **Test benches** (Animator F. Druillole)
  - ✓ T2K-like Test Bench (F. Druillole)
  - ✓ Architecture of the Apr./Dec. testBench
    - ✓ Test functions
    - ✓ Requirements for CoBo firmware
- 10:30-10:45 Break
- 10:45-11:30 **Test benches** cont. (Animator F. Druillole)
  - ✓ Test bench communication tools
  - ✓ Time Slice architecture
  - ✓ Framework time line for December test.
    - ✓ AGET AsAd test
    - ✓ CoBo test
    - ✓ MuTanT test
    - ✓ Integration test
- 11:30-12:45 **Calibration** (Animator B. Raine)
  - ✓ Control data processing Must2 example (B. Raine)
  - ✓ ANTARES architecture example (S. Anvar)
  - ✓ GET Calibration architecture (All)
- 12:45-14:00 Lunch
- 14:00-14:45 **Bottlenecks and strategy** (Animator JL Pedroza)
  - ✓ Technical bottleneck
    - ✓ Hardware
    - ✓ Software
    - ✓ Solutions
  - Resources bottleneck
    - ✓ Financial
    - ✓ Manpower
    - ✓ What can be shared
- 14:45-15:30 **Bottlenecks and strategy** (Cont. Parallel sessions)
  - ✓ Hardware sub session discussion
  - ✓ Software sub session discussion
- 15:30-15:45 Break
- 15h45-16:15 **Time Line** (Animator E. Pollacco)
  - ✓ Schedule
  - ✓ Input for others projects (GANIL-Spiral2 & NEXT)
- 16:15-16:30 **Summary of the meeting** (E. Pollacco, JL Pedroza)

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