T2K project

# Reduced Acquisition System to Read 288 channels of the Front End Card of T2K experiment

Applicated to read gazeous detectors.

Writer :

**Frédéric Druillole** CEA-Saclay IRFU/SEDI/LDEF

# lrfu

nstitut de



recherche sur les lois fondamentales de



# Abstract

The document explains to users how to install different drivers and software to control the FEC board based on the stuc daughter card. Cautious must be taken during the driver installation. Otherwise, the PC could be reinstalled completely.

It gives all information how the software works out in the DAQ T2K application. The software technology is based on XML and UDP/IP protocol. A command server run all the time during the processing of the system. A second software sends commands to the server to control the FEC board and its application to read out detectors channels.

*The Fem\_Stuc system is deployed uniquely under windows XP operating system.* 

# **Document Status Sheet**

1. Document Title: T2K 280 m TPC - Read-out Electronics - System and User Requirements Document								
2. Document Reference Number: EDMS blabla								
3. Issue	4. Revision	5. Date	6. Reason for change					
0	0	5 february 2009	Creation					

 Table 1 Document Status Sheet

# **Document Change Record**

Table 2 Document Change Record (of changes made since issue ... )

Document Change Record			DCR No.		
			Date		
			Originator		
			Approved By		
1. Document Title					
2. Document Reference Number			[Document Reference Number]		
3. Document Issue / Revision Number			[Document Issue]/[Document Revision Number]		
4. Page	5. Paragraph	6. Reason for	Change		

# **Table of Contents**

Abstract	iii
Document Status Sheet	iii
Document Change Record	iv
Table of Contents	v
Document Change Record Table of Contents	iv v

### **1.INTRODUCTION**

Fem\_STUC system is part of the T2K nd280 collaboration. The intent is to read out 288 channels from a gaseous detector. The vocation is not to have great performance in terms of speed but to help designers to test detectors.

The system consists of a mother board called FEMR\_STUC with a daughter board called STUC. The two board's aim is to control and read out data coming from the FEC board. The FEC board consists of four AFTER chips (288 channels) and an internal pulse generator to test and calibrate electronic channels. A high speed ADC makes the digitization stored in a memory inside FEMR\_STUC board.

The link between FEM\_STUC system and the PC is via an USB interface. Why USB? USB is easily controlled in modern computing and so having an acquisition system with USB is an advantage when your job is to do measurement instead of building instrumental system.

The Front End Board is very sensitive to all bad EMC environment. So a shield must be applied to the board. Several holes exist on the PCB to ease the mechanic design. (see picture). The shiels is not a deliverable of the set FEM\_STUC. *You must provide depending of your mechanical constraint your own shield*.

### **2. STUC DRIVER INSTALLATION**

DO NOT PLUG THE STUC CARD ON A PC BEFORE HAVING INSTALLED DRIVERS CORRECTLY. OTHERWISE, THE RE-INSTALLATION OF THE OPERATING SYSTEM COULD BE MANDATORY

The STUC probe is working only with Windows XP. No test has been done on Vista system. The delivered CD contains all necessary files to install the Fem\_Stuc application. You must at first install STUC driver and at second time, you could install Fem\_Stuc software to control the hardware.

### List of directories :

STUCV4Package contains all information and software concerning the STUC board and the associated drivers and software.

#### $\rightarrow$ Copy the directory STUC4Package into the c:/ root.

You will notice 7 directories inside.

- $\mu C$  CypressFx2 contains the embedded software to STUC. Two files exists.
  - Stuc2.hex: standard program for STUC.
  - Eeprom.hex : Programme to use STUC without USB (not useful for Fem\_STUC application)
- FPGA\_Files contains svf files and vhdl files for the firmware
  - Program Files contains svf files ready to use
  - VHDL\_Files contains intrinsic program to develop new application with the stuc hardware.

- Install contains drivers for the cypress component. (see below)
- Library contains two files to add STUC functions to your home made software application
- Projects contain all executables and sources for the command\_server application to the Fem\_STUC.
- CVI\_DRV contains all drivers to make interface with STUC working.

### STUC PROBE INSTALLATION

In Install directory, four files has to be copied at the right place as explained below .

**1**- you must place files as followed :

"ezmon.sys" into C:\WINNT\system32\drivers "ezusb.sys" into C:\WINNT\system32\drivers "ezusbw2k.inf" into C:\WINNT\inf "ezusbw2k.PNF" into C:\WINNT\inf

**2-** Plug the STUC usb connector in one input of the computer. A wizard material assistant window appears. (see below). Let tick off NO and press NEXT.



**3-** A new second window appears, indicating the driver for the new Cypress USB component (initial driver for the STUC probe). Let tick off YES, ONLY THIS TIME and press NEXT (see below). The STUC probe is installed.

At the first start of the Command\_server client software, a identical windows appears showing STUC-HIGH SPEED driver. Let tick off YES, ONLY THIS TIME.



## STUC PROBE LIBRARY INSTALLATION

In the CVI\_DRV directory, you will notice three others directories. Each name of directory indicates where to copy all containing files in the computer system. Hence :

• All files in PROG\_FILES\_COMFILES\_MERGE\_MODULE must be copied into the path:

C:/Program Files/Common Files/ Merge Modules

- All files in WINNT\_SYSTEM32\_DRIVERS must be copied into the path: C:/WINNT/System32/Drivers
- All files in WINNT\_SYSTEM32 must be copied into the path: C:/WINNT/System32

#### **Reboot your PC !!!**

### Now you are ready to use Fem\_STUC applications software.

### **3. HARWARE DESCRIPTION**

As said previously, the system consists of a Fem\_STUC (see below) system and a computer to acquire data. The Fem\_Stuc system is constituted of :

- Front End Board (FEC) embedded four AFTER circuit to handle 288 electonic channel to read out pads from a gazeous detector.
- A FEMR\_STUC with the STUC probe to control and manage the usb link to the computer.

T2K project

#### T2K project



#### FEM\_STUC SYSTEM



Front End Board (FEC) with 4 AFTER Chip. The board could be tested and calibrated via an internal pulser, leaded by the FEMR\_STUC board.



FEMR\_STUC. You must plug MM\_POL and RES\_S4 to VCC to switch on the photomos.

#### Fem\_STUC User Guide CEA-Saclay IRFU/SEDI FemStuc-User-Manuel.doc

#### T2K project



STUC PROBE with the cypress µcontroller and the Xilinx embedded firmware. Three connectors is necessary to plug its 200 pins.



Example of a shield for the FEC card. The mechanical structure could also include the FEMR\_STUC board.





View of the front end asic and its protection against sparks.

# **4. SOFTWARE HARCHITECTURE**

T2K project

To command the FEC hardware, you must download a software for the cypress µcontroller and the embedded firmware into the Xilinx. It is done inside the command\_server application. This software runs on PC and does the interface between client through UDP protocol and FEM\_STUC hardware throough USB link. It must run all the time you want to acquire data.

Client software runs through UDP network to send instruction to the command\_server application. It consists of an Unicode language to give orders to the firmware embedded on the FEM\_STUC hardware.



### **5. COMMAND SERVER USER GUIDE**

You will find the software under the path :

.... Projects/bin/t2k/dcc/winnt/dcc/cmd\_server.exe

The picture below gives you an example where the software downloaded all needed firmware inside STUC and recognized the hardware.

You must see before the last message "0 Fem<0> supports 1 FEC<s>" a window with a slide scale. It tracks the download of the software into the stuc probe.

Now you are ready to deploy client-application to take data.

## Fem\_STUC User Guide CEA-Saclay IRFU/SEDI FemStuc-User-Manuel.doc

CEA-Saclay IRFU/SEDI				
FemStuc-User-Manuel.doc				
iédente 🔻 🐑 👻 🔊 Rechercher 🖓 Dossiers 🛛 🎹 🗸				
🔤 🚥 cmd_server.exe				
DCC Command Interpreter (1 FEMs) (UDP/IP)				
or hostname=dappcj232 IP=132.166.9.232	hd_			
0 Fem(0) supports 1 FEC(s)				
op in the second se	nai.			
du				
nv l	test			
ec	. Ka			
15				
ls l				
	-			
51 ation				

# 6. CLIENT SOFTWARE – DAQT2K-

Cf others documentation