Technical workshop

November 18-19, 2009

Bordeaux

Nov 18 08:45 - 17 :30

- ➢ 08:45-09:00   **Introduction** (E. Pollacco, JL Pedroza)

- ➢ 09:00-09:45   **AGET** (P. Baron)
    - o Architecture
    - o Fast control /Hits details
    - o Status

- ➢ 09:45-10:30 **AsAd** (J. Pibernat)
    - o Architecture
    - o Tasks and organisation
    - o Status

- ➢ 10:30 -10:45   Break

- ➢ 10:45-11:15   **ZAP** (Animator JL Pedroza)
    - o Status (E. Delagnes)
    - o ZAP-AsAd coupling (JL Pedroza)

- ➢ 11:15-12:00   **CoBo** (N. Usher)
    - o Reduced CoBo architecture
    - o Firmware
    - o Embedded software
    - o DDR2 SDRAM access details
    - o status

- ➢ 12:00-12:45   **MuTanT** (G. Wittwer)
    - o MTCA architecture
    - o CoBo- MuTanT coupling
    - o MuTanT-MuTanT coupling
    - o Clock distribution and inspection lines details
    - o Status

- ➢ 12:45-14:00 Lunch

- ➢ 14:00-14:45   **Software Slow control** (S. Anvar)
    - o Slow control distributed software architecture (over both embedded and workstation platforms)
    - o Configuration management - interfacing run control, slow control and acquisition
    - o Status of slow control and configuration frameworks

- ➢ 14:45-15:30   **Software Run control** (F. Saillant)
    - o Run control architecture
    - o Data flow and embedded readout
    - o Status

- ➢ 15:30-15:45 Break

- ➢ 15:45-17:00 **Parallel sessions**
  - o Hardware sub session discussion
    - ▪ Particulars points
    - ▪ Interfaces
  - o Software sub session discussion
    - ▪ Particulars points
    - ▪ Interfaces

- ➢ 17:00-17:30
  - o **Summary and conclusions**

Nov 19 09:00-16:30

- ➢ 09:00- 09:30 2010 **Tasks schedule** (Animator JL Pedroza)
  - o Goal: 2010 Dec. GET prototype 1 Ready
  - o Milestone 2010 Apr.

- ➢ 09:30-10:30   **Test benches** (Animator F. Druillole)
  - o T2K-like Test Bench (F. Druillole)
  - o Architecture of the April/December testBench
    - ▪ Test functions
    - ▪ Requirements for CoBo firmware

- ➢ 10:30-10:45 Break

- ➢ 10:45-11:30 **Test benches** cont. (Animator F. Druillole)
  - o Test bench communication tools
  - o Time Slice architecture
  - o Framework time line for December test.
    - ▪ AGET AsAd test
    - ▪ CoBo test
    - ▪ MuTanT test
    - ▪ Integration test

- ➢ 11:30-12:45 **Calibration** (Animator B. Raine)
  - o Control data processing Must2 example (B. Raine)
  - o ANTARES architecture example (S. Anvar)
  - o GET Calibration architecture (All)

- ➢ 12:45-14:00 Lunch

- ➢ 14:00-14:45 **Bottlenecks and strategy** (Animator JL Pedroza)
  - o Technical bottleneck
    - ▪ Hardware
    - ▪ Software
    - ▪ Solutions

- o Resources bottleneck
  - ▪ Financial
  - ▪ Manpower
  - ▪ What can be shared

- ➢ 14:45-15:30 **Bottlenecks and strategy** (Cont. Parallel sessions)
  - o Hardware sub session discussion
  - o Software sub session discussion

- ➢ 15:30-15:45 Break

- ➢ 15h45-16:15   **Time Line** (Animator E. Pollacco)
  - o Schedule
  - o Input for others projects (GANIL-Spiral2 & NEXT)

- ➢ 16:15-16:30 **Summary of the meeting** (E. Pollacco, JL Pedroza)