Visio/phone meeting 2009 for GET 13th Jan 2009, 14:00-16:00 (French-time)

Minutes

Present: W. Mittig, A. Bickley, N. Usher, B. Lynch, P. Baron, F. Druillole, R. Raabe, F. Saillant, G. Wittwer, R. Lemmon, J. Pibernat, JL Pedroza

AsAd/AGET:

The \sum of the trigger is built inside AGET then sent to ADC during the SCA writing process (through an internal analogue MUX). The frequency set in simulation is the same for the ASIC reading (25MHz). The tests functional and noise are good (P. Baron, IRFU). Discussion about the front-end connections to the detectors. Use of flexible PCB, the capacity must be evaluated, needs to build a prototype for test.

J. Pibernat, CENBG writes the technical specification for AsAd.

Possibility to evaluate a high density pads card with a prototype (CENBG, IRFU F.

Druillole). Pads size has to be discussed between physicists

TI ADC: good product but the conversion mode has to be tested (T&H or S&H), N. Usher, MSU

Cooling: must be done by fans, maybe problem with vibrations? Must be evaluated. It is better to cool the whole front-end system, no only the ASIC.

Planned adaptation to test AsAd on the FEC test bench (CENBG, IRFU)

ZAP:

A project on planned for AGET input protection (sparks). Has to be followed.

MUTANT:

G. Wittwer, GANIL works on the first and second level trigger in MUTANT, he will write a paper for the next workshop

Cobo:

N. Usher, MSU is working on the Cobo firmware FPGA and the slow control. See the minutes of the 2008, Sept 25th and documents in the MSU WIKI

Sofware:

Meeting held at IRFU, see documents on the wiki. Collaboration between GANIL and IRFU for the software.

Organisation:

Next visio/phone meeting Jan 27th. Please look at http://groups.nscl.msu.edu/tpc/wiki/doku.php?id=periodic_meetings for the connection parameters and write the topics you want in the agenda.