

Visio/phone meeting 2009 24th Mar 2009, 14:00-16:00 (French-time)

Minutes

Present : N. Usher, B. Lynch, F. Druillole, R. Raabe, E. Pollacco, B. Raine, G. Wittwer, R. Lemmon, P. Coleman-Smith, T. Murakami, J. Pibernat, <u>JL Pedroza.</u>

Preamble: Some problem with the connection parameters and access codes, they were changed between publication on the Wiki and date of meeting...hope this is the first and last time.

Agenda:

- Caen GET workshop debriefing.
 - Global & funding
 - o Physics
 - o Technical
 - o Reviewers opinions
- Organisation
 - First proposed schedule for 2009-begining of 2010.
 - Improving communication

I. Caen GET workshop debriefing

Very good meeting and picture on GET but some points need clarification. 1) Global

To clarify or add on the workpackage

- Pulser : presentation to synthetize
- 12 or 14 bits for ADC ?
- Transfer rate from hit channels to CoBo
- AGET for 2p radioactivity type, usage of the ring buffer: size, consequences on the number of channel by chip.
- BEM and absolute time stamp: could be used or no on BEM? See meeting at GSI and look how to introduce the better at GANIL, that is including or no an external system or using the GET internal system (price, complexity etc.)
- InBo is now a pure software subsystem which needs to develop a point to point communication protocol. The switcher solution is a new task (a task group must be defined).
- Some task must be redefined with a redistribution :
 - ➢ CoBo (retasking)
 - Software (retasking)
 - Separate subsystems from slow control for "vital" monitoring (environment control, temperature, supplies..)
- 5 news task appear :
 - ➢ Gaz amplification

- Electro mechanics
- ➢ Calibration
- > Monitoring
- ➢ Switch system

Need to ad people to manage these 5 tasks

2) Funding: ANR request seems to be on the good trajectory

Relations with institutions:

MSU and RIKEN must have the MoU to continue. The MoU required an annex which describes the different hardware, Software, Test, quality, documentation. Need to discuss again with RIKEN for possible contribution.

3) Physics

In others words, what appends near the detectors:

- Electromagnetic fields: we know the value and consequences for the frontend
- High voltage: no information at this time, needs an external protection? ZAP...

4) Technical

AGET: readout of the hit channel address

The readout of hit channels uses the slow control lines and this solution is limited in frequency. P. Baron suggests a faster solution (must be validated, look at the wiki)

Documentation: all documents must have the same standard, approved by all laboratories. A template is proposed in the wiki (F.druillole)

Questions on the data flow and the dead time: minimize the number of channel per ASIC? Increase the compression and reduction of the data? how? P. Coleman-Smith suggests a document written by Simon Letts.

Connections and communication between CoBo and PC farm: The data rate provided is too low, a discussion should be continued between hardware and software designers, especially:

- 1 or 2 ethernet links from CoBo to the PC farm
- DSP implementation on CoBo

N. Usher will propose a list of items from CoBo to discuss between designers.

Master MUTANT and some slave MUTANT (G. Wittwer): this solution is more compact and easy to implement, but requires a large number of communication channels and cables.

Sofware:B. Raine and S. Anvar should discuss to improve the distribution of software tasks.

5) Reviewers opinions (P. Coleman-Smith)

The system as presented is very complex and a lot of thought has gone into the generation of the drafts. Some thought concerning the system test and calibration including the trigger setup would be useful.

The System document from Saclay doesn't match up with the CoBo and MuTanT when discussing the trigger mechanism, communications and the system calibration. I expect this will be addressed in the full versions of the documents. There is a lack of clarity about the event rates needed in the different experiments and the effect these have on the rate the equipment operates. The system document discusses the trigger methods and needs some clarification. A graphical representation of the interplay

between the % of active ASIC channels, the clustering of channels within an ASIC, the readout of specific channels and the compression needed to move the data off the CoBo would be a help. Regarding the CoBo requiring two Ethernet ports and hence two processors. If the use of these two communications routes is considered isn't it the case that slow control to alter the setup generally means that data from the experiment is not really important. So this would usefully be shared Transfer of data between two FPGAs within the board increases the complexity. Two Ethernet ports also doubles the number of network switches required.

II Organisation

1) First proposed schedule for 2009-begining of 2010. (F. Druillole)

N°	0	WBS	Nom de la tâche	Durée	Début	Janvier	Mars	Mai	Juilet	Septembre	Novembre	Janvier	Mars	Mai
1	•	1	Banc de Test AGET	180 jours	Mer 01/04/09									
2	31	1,1	soumission asio	0 jour	Lun 20/07/09				20/07					
3		1.2	reception asic	0 jour	Jeu 15/10/09					•	15/10			
4		1.3	réalisation PCB FEC TEST	3 mois	Mer 01/04/09				Frédéric Dru	uillole				
5	111	1.4	achat kit xilinx FX virtex 5	0 jour	Lun 08/06/09			♠_0	06					
6		1.5	Upgrade Firmware + software	4 mois	Lun 08/08/09			_		Fréd	éric Druillole			
7		1.6	conception banc de test	4 mois	Mer 24/08/09					F	rédéric Druill	ole;Pascal B	aron	
8		1.7	Mise au point banc de test	1 mois	Mer 14/10/09						Frédéric	Druillole;Pa	scal Baron	
9		1.8	Test Préliminaire AGET	1 mois	Mer 11/11/09						Fri	déric Druill	ole;Pascal Ba	ron
10														
11		2	AGET Specification finale	15 jours	Mer 01/04/09			Pascal Baron	1					
12		3	AsAd Specification final	1 mois	Mer 01/04/09			Jérôme Pil	bernat;Jean-l	louis Pedroz	a			
13		4	CoBo Specification finale	1 mois	Mer 01/04/09			Abigail ;Na	than Usher					
14		5	MUTANT specification finale	2 mois	Mer 01/07/09					Gilles Witty	ver			
15	111	6	BEM specification finale	2 mois	Lun 03/08/09					Gilles	s Wittwer			
16		7	Slow control HW et SW Specification	3 mois	Mer 29/04/09				Frédé	ric Château;	Shebli Anvar			
17		8	Run Control HW et SW Specifiation	3 mois	Mer 22/07/09					B	runo Raines;	Frédéric Sai	llant	
18														
19														
20		9	Management	288 jours	Mer 01/04/09		-		1				-	•
21		9.1	MOU	2 mois	Mer 01/04/09			Patri	cia Chomaz-F	Roussel;Emn	nanuel Pollac	co		
22		9.2	Coût	5 mois	Mer 01/04/09			anna <mark>anna anna a</mark>		Emmaruel P	ollacco;Patric	ia Chomaz-	Roussel	
23		9.3	système supervision	288 jours	Mer 01/04/09					ļa sa s			<u>.</u>	Frédéric 🕻
24		9.4	Elaboration des frames documentaires	2 mois	Mer 01/04/09			Fréd	éric Druillole					
25														
26														
27	•	10	Banc de test GET -I	288 jours	Mer 29/04/09			-					· · ·	
28	111	10.1	CoBo pour UNE AsAd	12 mois	Ven 29/05/09			areas a	÷					Nathan Ush
29		10.2	AsAd fabrication	8 mois	Mer 29/04/09			7			Jé	rôme Piberi	nat	
30		10.3	AsAd test electrique	7 jours	Mer 09/12/09						Ľ.	érôme Pibe	rnat;Jean-lo	uis Pedroza
31		10.4	AsAd test fonctionel	7 jours	Ven 18/12/09						1	Jean-louis	Pedroza;Jér	ôme Piberna
32		10.5	MuTant firmware	9 mois	Lun 28/09/09									Gille

GANIL must ask for CDD as soon as possible and must organize meetings with Saclay for the software tasks.

2) Improving communication (E. Pollacco)

It seems now necessary that the participants in the different tasks take the initiative to start working together through the phone & e-mail. They also should have mini-telephone conferences. Once the issues are settled then the VC conferences should include larger audience. It would be more efficient & direct.

2 Contact Working Groups can be considered:

- CoBo AsAd interface & communication (MSU, CEA, GANIL)
- Acquisition CoBo and SWIM (SWIM is SWItch Module)

Moreover, Riken will clarify its participation soon.